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					APPLICANT Uming Ko, et al					
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	AS S. Shigematsu, et al., "A 1-V High-Speed MTCMOS Circuit Scheme for Power-Down Application Circuits," <i>IEEE Journal of Solid State Circuits</i> , vol. 32, No. 6, pp. 861-869, June 1997.									
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EXAMINER DATE CONSIDERED 9(4/1)										
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.										

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